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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/479,146	01/07/2000	STEPHEN FULD	99-051-TAP	2688
7	590 03/18/2003			
TIMOTHY R SCHULTE STORAGE TECHNOLOGY CORPORATION ONE STORAGETEK DRIVE MS 4309 LOUISVILLE, CO 800284309			EXAMINER	
			MASKULINSKI, MICHAEL C	
LOUIS VILLE, CO 800284309			ART UNIT	PAPER NUMBER
			2184	11

Please find below and/or attached an Office communication concerning this application or proceeding.

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•	Application No.	Applicant(s)				
	09/479,146	FULD, STEPHEN				
Office Action Summary	Examiner	Art Unit				
	Michael C Maskulinski	2184				
The MAILING DATE of this communication appeared for Reply	ppears on the cover sheet with	the correspondence address				
A SHORTENED STATUTORY PERIOD FOR REP	LY IS SET TO EXPIRE 3 MON	ITH(S) FROM				
THE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CFR 1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a re - If NO period for reply is specified above, the maximum statutory perio - Failure to reply within the set or extended period for reply will, by statu. - Any reply received by the Office later than three months after the mail earned patent term adjustment. See 37 CFR 1.704(b). Status	1.136(a). In no event, however, may a reply eply within the statutory minimum of thirty (3 d will apply and will expire SIX (6) MONTHS ate, cause the application to become ABAN	be timely filed 0) days will be considered timely. 6 from the mailing date of this communication. DONED (35 U.S.C. § 133).				
1) Responsive to communication(s) filed on 18	3 February 2003 .					
2a)⊠ This action is FINAL 2b)□ 1	This action is non-final.					
3) Since this application is in condition for allow						
closed in accordance with the practice unde Disposition of Claims	er <i>Ex par</i> te <i>Quayle</i> , 1935 C.D.	11, 453 O.G. 213.				
4)⊠ Claim(s) <u>12-14 and 16-18</u> is/are pending in t						
4a) Of the above claim(s) is/are withdr	awn from consideration.					
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>12-14 and 16-18</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and	or election requirement.					
Application Papers	a or					
9) The specification is objected to by the Examir10) The drawing(s) filed on <u>07 January 2000</u> is/ar		d to by the Evaminer				
Applicant may not request that any objection to						
11) The proposed drawing correction filed on						
If approved, corrected drawings are required in I						
12) The oath or declaration is objected to by the E						
Priority under 35 U.S.C. §§ 119 and 120						
13) Acknowledgment is made of a claim for forei	gn priority under 35 U.S.C. § 1	19(a)-(d) or (f).				
a) All b) Some * c) None of:						
1. ☐ Certified copies of the priority docume	nts have been received.					
2. Certified copies of the priority docume	nts have been received in App	lication No				
Copies of the certified copies of the pri application from the International E See the attached detailed Office action for a list	Bureau (PCT Rule 17.2(a)).					
4) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).						
a) The translation of the foreign language p	provisional application has been	n received.				
Attachment(s)	p a.i.a 55 5.6.5. 33	,				
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Info	nmary (PTO-413) Paper No(s) rmal Patent Application (PTO-152)				

Application/Control Number: 09/479,146

Art Unit: 2184

Non-Final Office Action

Claim Rejections - 35 USC § 103

- 1. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
- 2. Claims 12-14 and 16-18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Stolowitz, U.S. Patent 6,018,778, and further in view of White, How Computers.work.

Referring to the limitation "the storage elements are magnetic tape drives or a track of a magnetic tape" of claims 12 and 16, in the Abstract, Stolowitz discloses a disk drive array. Further, in Figure 5, Stolowitz discloses a multiplexer (510) for changing the data from a parallel state to a serial state. However, Stolowitz doesn't explicitly disclose a magnetic tape having data blocks and a parity block in which the data blocks and the parity block are serially arranged on the magnetic tape with the parity block following the data blocks and the parity block being based on the data blocks. On pages 176-177, White discloses that the format of a QIC tape typically contains 20 to 32 parallel tracks. Each track is divided into blocks of 512 or 1,024 bytes, and segments typically contain 32 blocks. Of the blocks in a segment, eight contain error-correction codes. These tracks comprise both data and parity. It would have been obvious to one of ordinary skill at the time of the invention to use a magnetic tape in the redundant storage system of Stolowitz. A person of ordinary skill in the art would have been motivated to make the modification because in column 8, lines 32-33, Stolowitz discloses the use of a serial stream when reading from the disk drives which is

Application/Control Number: 09/479,146 Page 3

Art Unit: 2184

necessary for a tape drive. Thus there is a means for changing parallel data from the disks to serial data. The serial data from a tape would also be able to be inputted into the system of Stolowitz. Further, on pages 12-13 and in Figure 5, the Applicant discloses that an obvious variation of the magnetic tape drive is an array of disks. Specifically, on page 13, lines 5-7, the Applicant discloses that *controller 18 writes to and reads from storage elements in the same manner as described with reference to the track of magnetic tape 14 in FIG.* 3. Also, the system of Stolowitz is compatible with a tape disk drive because it contains a SCSI bus, which is a common interface for devices such as CD-ROM, drives and backup tape drives as well as hard disks (see column 4, lines 15-17).

Referring to the remaining limitations in claims 12 and 16:

- a. On page 176, White discloses that of the blocks in a segment, eight contain error correction codes (the parity block following the data block).
- b. In the Abstract, Stolowitz discloses a disk drive array with parity data based upon data blocks and a disk drive array controller that carries out disk drive data transfers.
- c. In column 6, lines 20-22, Stolowitz discloses methods and circuitry for effecting synchronous data transfer to and from an array of disk drives (reading blocks sequentially from respective data storage elements).
- d. In column 8, lines 42-44, Stolowitz discloses reconstructing missing data in the event of any single drive failure (determining if the data block currently being read is good or bad).

Page 4

Application/Control Number: 09/479,146

Art Unit: 2184

e. In column 8, lines 42-44, Stolowitz discloses that the serialized read data stream is passed through an N+1 stage pipeline register—data being entered shifts old data out (providing the data block currently being read to the host if the currently being read data block does not follow a bad data block).

- f. In column 8, lines 50-55, Stolowitz discloses that once the data from the last drive enters the pipeline, the accumulator will be holding the data from the missing drive. This result is transferred to a hold latch, and when the missing word in the pipeline from the failed drive is reached, the contents of the hold latch is substituted in place of the pipeline contents (if one of the data blocks is bad, storing the good data blocks following the bad block in sequential order).
- g. In column 8, lines 42-48, Stolowitz discloses that to reconstruct missing data in the event of any single drive failure, the serialized read data stream is passed through an N+1 stage pipeline register. To begin, a word from the first drive is loaded into an accumulator and into the pipeline. As the next data word enters the pipeline from the next drive, it is XORed with the first word and the result stored in an accumulator.
- h. In column 8, lines 50-55, Stolowitz discloses that once the data from the last drive enters the pipeline, the accumulator will be holding the data from the missing drive (reading the parity block from the magnetic tape after all of the data blocks have been read).
- i. In column 8, lines 48-55, Stolowitz discloses that the accumulating process is repeated for each subsequent drive except that data from the failed

Art Unit: 2184

drive is ignored. Once the data from the last (redundant) drive enters the pipeline (reading the parity block from the parity storage element), the accumulator will be holding the data from the missing drive. This result is transferred to a hold latch, and when the missing word in the pipeline from the failed drive is reached, the contents of the hold latch is substituted in place of the pipeline contents (if one of the data blocks is bad, reconstructing the bad data block from the accumulated parity of the good data blocks and the parity block in order to form a reconstructed good data block; providing the reconstructed good data block to the host; and providing the stored good data blocks to the host in sequential order after the reconstructed good data block has been provided to the host).

j. In column 8, lines 42-44, Stolowitz discloses an N+1 stage pipeline register (a buffer for storing the good data blocks read by the controller after the bad data block until the controller reconstructs the bad data block to preserve ordering of the data blocks during reading).

Referring to claim 13, in column 8, lines 42-48, Stolowitz discloses that to reconstruct missing data in the event of any single drive failure, the serialized read data stream is passed through an N+1 stage pipeline register. To begin, a word from the first drive is loaded into an accumulator and into the pipeline. As the next data word enters the pipeline from the next drive, it is XORed with the first word and the result stored in an accumulator (accumulating parity of the good data blocks includes exclusive ORing the parity of the good data blocks read prior to the good data block currently being read with the good data block currently being read).

Application/Control Number: 09/479,146

Art Unit: 2184

Referring to claim 14, in column 8, lines 48-55, Stolowitz discloses that the accumulating process is repeated for each subsequent drive except that data from the failed drive is ignored. Once the data from the last (redundant) drive enters the pipeline, the accumulator will be holding the data from the missing drive. This result is transferred to a hold latch, and when the missing word in the pipeline from the failed drive is reached, the contents of the hold latch is substituted in place of the pipeline contents (reconstructing a bad data block includes exclusive ORing the accumulated parity of the good data blocks and the parity block).

Referring to claim 17, in column 8, lines 42-48, Stolowitz discloses that to reconstruct missing data in the event of any single drive failure, the serialized read data stream is passed through an N+1 stage pipeline register. To begin, a word from the first drive is loaded into an accumulator and into the pipeline. As the next data word enters the pipeline from the next drive, it is XORed with the first word and the result stored in an accumulator (the parity accumulator accumulates parity of the good data blocks by exclusive ORing the parity of the good data blocks read prior to the good data block currently being read with the good data block currently being read).

Referring to claim 18, in column 8, lines 48-55, Stolowitz discloses that the accumulating process is repeated for each subsequent drive except that data from the failed drive is ignored. Once the data from the last (redundant) drive enters the pipeline, the accumulator will be holding the data from the missing drive. This result is transferred to a hold latch, and when the missing word in the pipeline from the failed drive is reached, the contents of the hold latch is substituted in place of the pipeline contents

Art Unit: 2184

(reconstructing a bad data block includes exclusive ORing the accumulated parity of the good data blocks and the parity block).

Response to Arguments

3. Applicant's arguments with respect to claims 12 and 16 have been considered but are most in view of the new ground(s) of rejection.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael C Maskulinski whose telephone number is (703) 308-6674. The examiner can normally be reached on Mon-Thu 7:30-5 and Fri. 7:30-4 (second Fri.).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Beausoliel can be reached on (703) 305-9713. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 746-7239 for regular communications and (703) 746-7238 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

Application/Control Number: 09/479,146

Art Unit: 2184

MM

March 12, 2003

Page 8

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SUPERVISORY PATENT EXAMINER

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